2SA1790G

Silicon PNP epitaxial planar type

For high-frequency amplification Complementary to 2SC4626G

■ Features

- Optimum for RF amplification of FM/AM radios
- High transition frequency f_T
- SS-Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	-30	V	
Collector-emitter voltage (Base open)	V _{CEO}	-20	V	
Emitter-base voltage (Collector open)	V_{EBO}	-5	V	
Collector current	I_C	-30	mA	
Collector power dissipation	P _C	125	mW	
Junction temperature	T_{j}	125	°C	
Storage temperature	T_{stg}	-55 to +125	°C	

■ Package

- Code SSMini3-F3
- Marking Symbol: E
- Pin Name
 - 1. Base
 - 2. Emitter
 - 3. Collector

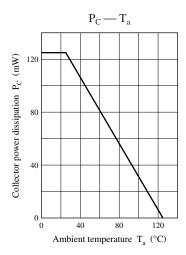
■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

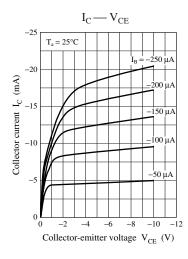
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Base-emitter voltage	V_{BE}	$V_{CE} = -10 \text{ V}, I_{C} = -1 \text{ mA}$		- 0.7		V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = -10 \text{ V}, I_E = 0$			- 0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = -20 \text{ V}, I_B = 0$			-100	μΑ
Emitter-base cutoff current (Collector open)	I_{EBO}	$V_{EB} = -5 \text{ V}, I_C = 0$			-10	μΑ
Forward current transfer ratio *	h _{FE}	$V_{CE} = -10 \text{ V}, I_{C} = -1 \text{ mA}$	70		220	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = -10 \text{ mA}, I_B = -1 \text{ mA}$		- 0.1		V
Transition frequency	f_T	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 200 \text{ MHz}$	150	300		MHz
Noise figure	NF	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 5 \text{ MHz}$		2.8	4.0	dB
Reverse transfer impedance	Z_{rb}	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 2 \text{ MHz}$		22	50	Ω
Reverse transfer capacitance (Common emitter)	C _{re}	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 10.7 \text{ MHz}$	·	1.2	2.0	pF

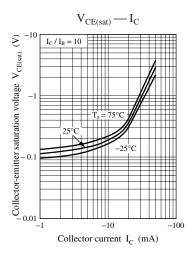
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

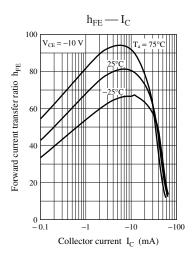
2. *: Rank classification

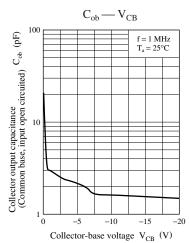
Rank	В	С
h_{FE}	70 to 140	110 to 220







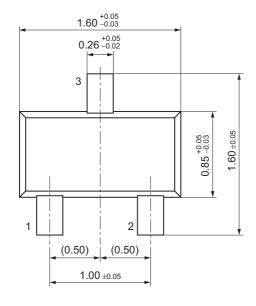


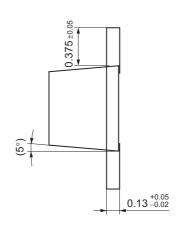


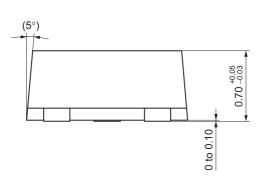
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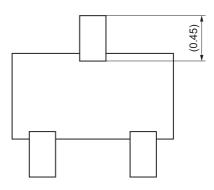
SSMini3-F3

Unit: mm









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